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Features a chapter on field-programmable logic devices, their technologies and architectures. Includes checklists, hints, and warnings for various design situations., Top-Down VLSI layout: From Architectures to Gate-Level Circuits and FPGAs represents a distinct method of studying electronic layout. built from greater than twenty years educating circuit layout, health professional Kaeslin's method follows the traditional VLSI layout stream and makes circuit layout obtainable for pros with a history in platforms engineering or electronic sign processing., Digital Integrated Circuit Design , Kenneth William Martin, 2000, Technology & Engineering, 543 pages. The impact of digital integrated circuits on our modern society has been pervasive., Top-Down VLSI Design: From Architectures to Gate-Level Circuits and FPGAs represents a unique approach to learning digital design. Developed from more than 20 years teaching circuit design, Doctor Kaeslin's approach follows the natural VLSI design flow and makes circuit design accessible for professionals with a background in systems engineering or digital signal processing.

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